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NOTES/COMMENTS:

Docket #	P020.03.AB	By:	MEM	Faxing Date	January 17, 2006
Ser. No:	10/634,636	Filing Date:	08/04/2003	Inv(s)	Rinerson et al..
Title:	A 2-Terminal Trapped Charge Memory Device with Voltage Switchable Multi-Level Resistance				

The following is being transmitted to the U.S. Patent Office:

Item	Description	# Pgs
1.	Supplemental Application Data Sheet	5
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SUPPLEMENTAL APPLICATION DATA SHEETRECEIVED
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Application Information

Application Number:: 10/634,636
Filing Date:: 08/04/2003
Application Type:: Regular
Subject Matter:: Utility
CD-ROM or CD-R?: None
Title:: A 2-Terminal Trapped Charge Memory Device
with Voltage Switchable Multi-Level Resistance

~~Attorney Docket Number:: UNTYP020~~**Attorney Docket Number:: P020.03.AB**

Request for Early Publication?: NO
Request for Non-Publication?: NO
Total Drawing Sheets:: 6
Small Entity?: YES
Petition Included?: NO
Secrecy Order in Parent Appl.?: NO

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Domestic Priority Information

Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Continuation-in-part of	10/249,846	Filing Date
10/249,846	An application claiming the benefit under 35 USC 119(e)	60/400,849	08/02/02
10/249,846	An application claiming the benefit under 35 USC 119(e)	60/422,922	10/31/02

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